

(19) World Intellectual Property Organization
International Bureau(43) International Publication Date
10 July 2003 (10.07.2003)

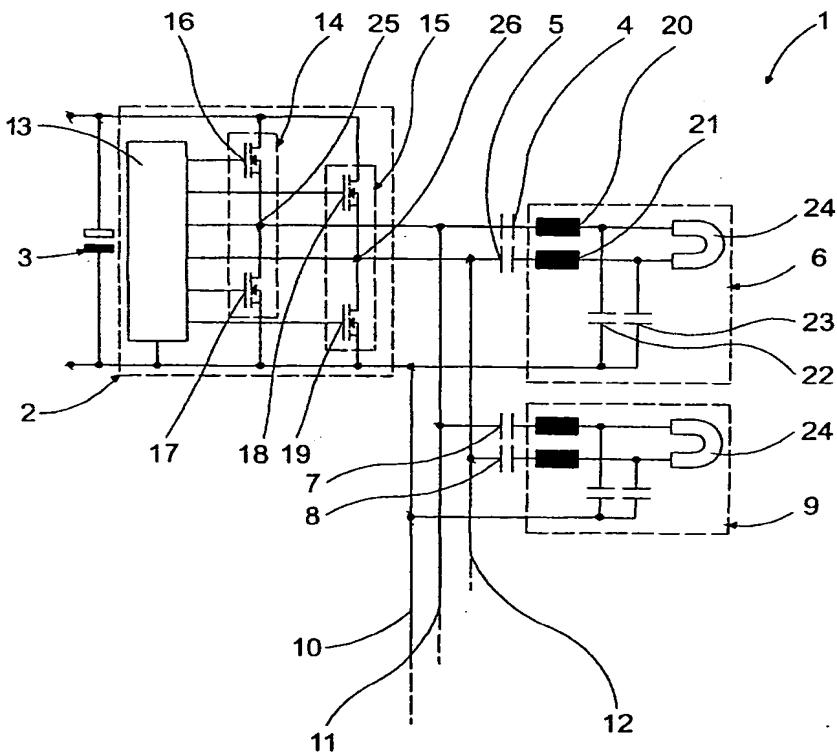
PCT

(10) International Publication Number
WO 03/056885 A1

- (51) International Patent Classification⁷: **H05B 41/282**
- (21) International Application Number: **PCT/IB02/05467**
- (22) International Filing Date:
18 December 2002 (18.12.2002)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
102 00 022.0 2 January 2002 (02.01.2002) DE
- (71) Applicant (for DE only): **PHILIPS INTELLECTUAL PROPERTY & STANDARDS GMBH [DE/DE]**; Stein-damm 94, 20099 Hamburg (DE).
- (71) Applicant (for all designated States except DE, SI, US): **KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]**; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **BÖKE, Ulrich [DE/DE]**; c/o Philips Intellectual Property & Standards GmbH, Weiss Hausstr. 2, 52066 Aachen (DE). **BOCK, Autoon [DE/DE]**; c/o Philips Intellectual Property & Standards GmbH, Weiss Hausstr. 2, 52066 Aachen (DE).
- (74) Agent: **VOLMER, Georg**; Philips Intellectual Property & Standards GmbH, Weiss Hausstr. 2, 52066 Aachen (DE).
- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,

[Continued on next page]

(54) Title: CIRCUIT ARRANGEMENT FOR OPERATION OF ONE OR MORE LAMPS



(57) Abstract: The invention relates to a background lighting system for a liquid crystal display, more particularly to an electronic circuit for operation of one or more discharge lamps. A DC/AC full-bridge inverter circuit generates two voltages whose AC components are phase-shifted by 180°. The discharge lamps are supplied with the sum of these two AC voltages.



LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

- (84) **Designated States (regional):** ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SI, SK,

TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— *with international search report*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Circuit arrangement for operation of one or more lamps

The invention relates to a circuit arrangement for operating one or more low-pressure gas discharge lamps, comprising a current converter and a driving device for the current converter.

5

Such a circuit arrangement for operating one or more low-pressure gas discharge lamps is known from DE 44 36 463 A1. This particularly relates to a circuit arrangement which is suitable for operation of compact low-pressure gas discharge lamps whose operating voltage exceeds the AC voltage generated by the converter and is suitable 10 for the operation of miniature phosphor lamps. In these circuit arrangements the principle of resonance step-up is used not only for generating the ignition voltage necessary for the low-pressure gas discharge lamp, but also for supplying the operating voltage of the lamp. This implies a reactive power flux at the operating voltage.

High voltages can also be generated by using a transformer such as described 15 in US 6,181,079 B1. Such transformers are awkward and heavy.

It is therefore an object of the invention to indicate a simple circuit arrangement for igniting and operating such lamps. More particularly a circuit arrangement is indicated that feeds a plurality of low-pressure gas discharge lamps in the background 20 lighting of a liquid crystal display from a voltage source.

This object is achieved in accordance with the characteristic features of claim 1. According to the invention a second current converter generates a voltage shifted by 180°.

Liquid crystal displays, also called LCDs for short, are nowadays also used as 25 liquid crystal picture screens. The liquid crystal picture screens are passive display systems i.e. they do not light up by themselves. These picture screens are based on the principle that light either passes the layer of liquid crystals or not. This means that an external light source is necessary for producing a picture. For this purpose an artificial light is generated in the background lighting system. With an increasing size of the liquid crystal picture screens, also

the performance level for the background lighting system of such picture screens increases. Lamps of small diameter are desired for these background lighting systems. Compared to other low-pressure gas discharge lamps in lighting arrangements, low-pressure gas discharge lamps in background lighting systems of liquid crystal picture screens have a smaller inner 5 diameter from 2 mm to 3.5 mm and, therefore, four to eight times higher lamp voltages. Thinner lamps for LCDs such as Ceralight lamps as known from EP 1 263 021 A1 work with 10 300 to 400 volts operating voltage, and cold cathode lamps in the following called Cold Cathode Fluorescent Lamps or CCFLs for short, work with 600 to 800 volts operating voltage. The ignition voltages to start these lamps are moreover higher by a factor of two. 15 These high ignition and operating voltages for thin low-pressure gas discharge lamps are generated without a transformer in that the low-pressure gas discharge lamps are supplied with power by two series-connected AC voltages. Since the two AC voltages have a 180° phase difference, the sum of the two AC voltages is applied to the low-pressure gas discharge lamp. In addition, these AC voltages are generated with moderate reactive power flux in the 20 resonant circuits. For this purpose, the circuit arrangement has low power losses and thus a smaller thermal load in the closed housing of the liquid crystal picture screen.

A circuit arrangement advantageously converts DC voltage into AC voltage and feeds one or several lamps which use a full-bridge switching circuit of power switches as a current converter and two resonant circuits per lamp, each of the resonant circuits 25 comprising one series-connected coil, one series-connected capacitor and one parallel-connected capacitor. This circuit arrangement comprises one full-bridge current converter and one resonant circuit per lamp. This provides that any number of lamps can be operated with a single current converter. This converter is thus scalable. The advantage of the full-bridge converter is that it generates a double output voltage compared to a half-bridge converter, without utilizing a transformer. The two half bridges work with 180° phase 30 distance. The ignition of the lamps and the power flux at normal operation is controlled by the switching frequency. The input impedance of the resonant circuit is then always ohmic inductive to have the power semiconductors of the full-bridge converter operate with minimum switching losses. This configuration has the advantage of a lower voltage load of the parallel capacitors.

The resonant circuits can additionally be constructed in three further circuit arrangements. Advantageously, a second circuit arrangement converts DC current into AC current and feeds one or more lamps which utilize a full-bridge circuit of power switches as a

current converter, two series-connected capacitors and two resonant circuits per lamp, each of the resonant circuits comprising a series-connected coil and a parallel-connected capacitor.

A third circuit arrangement advantageously converts DC current into AC current and feeds one or more lamps which utilize a full-bridge switching circuit comprising power switches as a current converter and one resonant circuit per lamp, which resonant circuit comprises one series-connected coil, one series-connected capacitor and one parallel-connected capacitor.

A fourth circuit arrangement advantageously converts DC current into AC current and feeds one or more lamps which utilize a full-bridge switching circuit with power switches as a current converter, two series-connected capacitors and one resonant circuit per lamp, which resonant circuit comprises one series-connected coil and one parallel-connected capacitor.

The parallel-connected capacitor is advantageously formed at least partly by a parasitic capacitance between the lamps and a metallic portion, thus the lamp electrodes and the electrically conductive parts of the display, for example, of the reflector.

To better understand the invention, an example of embodiment will be further explained hereinbelow with reference to the drawing in which:

Fig. 1 shows a circuit arrangement for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps,

Fig. 2 shows a timing diagram with a rectangular signal waveform,

Fig. 3 shows a timing diagram with a sine curve,

Fig. 4 shows a timing diagram with two sine curves phase-shifted by 180°,

Fig. 5 shows a second circuit arrangement for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps,

Fig. 6 shows a third circuit arrangement for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps,

Fig. 7 shows a fourth circuit arrangement for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps, and

Fig. 8 shows a diagram with a voltage ratio plotted against frequency.

Fig. 1 shows an electronic circuit arrangement 1 comprising a full-bridge switching circuit 2, a voltage source 3, two low-pass filters 4 and 5, a first lamp switching circuit 6, two further low-pass filters 7 and 8 and a second lamp switching circuit 9.

Electrically conducting lines 10, 11 and 12 lead to further lamp switching circuits (not shown).

The full-bridge switching circuit 2 also called full-bridge inverter in the following, comprises a control circuit 13 and two current converters 14 and 15. The current converter 14, in the following also called inverter, includes two power switches 16 and 17, and the second inverter 15 also includes two power switches 18 and 19. Power semiconductors such as bipolar transistors, IGBTs (Integrated Gate Bipolar Transistors) are also MOSFETs are used as power switches. The first lamp switching circuit 6 includes two series-connected coils 20 and 21, two parallel-connected capacitors 22 and 23 and one low-pressure gas discharge lamp 24. The second lamp circuit 9 has a similar structure with components 20 to 24. The control circuit 13 controls the first inverter 14 so that the power semiconductors 16 and 17 open and close in a push-pull mode. A rectangular signal waveform evolves at a node 25 between the power semiconductors 16 and 17. The control circuit 13 controls the second inverter 15 so that the power semiconductors 18 and 19 also open and close in a push-pull mode. A rectangular signal waveform also evolves at a node 26 between the power semiconductors 18 and 19. The two inverters 14 and 15 work in phase opposition, so that two rectangular signal waveforms evolve shifted by 180°. The low-pass filters 4, 5, 7 and 8 filter out the high-frequency components, so that two sinusoidal signals shifted in phase by 180° reach the lamps 24. The series-connected coil 20 and the parallel-connected capacitor 22 form a first resonant circuit 20, 22, the coil and the capacitor 23 form a second resonant circuit 21, 23. The low-pass filters 4 and 5, the coils 20 and 21 and the lamp 24 are connected in series between the two nodes 25 and 26. The capacitors 22, 23 are connected in parallel to the lamp 24 and to the minus pole of the DC voltage source 3. The half lamp voltage is applied via the capacitors 22 and 23, respectively.

Fig. 2 shows a rectangular signal waveform 31 which arises at the node 25. A similar signal waveform arises at node 26. The two rectangular signal waveforms are phase-shifted by 180°.

Fig. 3 shows a sinusoidal signal waveform 32 which evolves as a result of the smoothing by the low-pass filter 4.

Fig. 4 shows a sine curve 32 and a second sine curve 33 shifted by 180°, which is filtered by the low-pass filter 5. In this way a maximum voltage amplitude 34 corresponding to the value of the voltage supply 3 arises at the lamp 24.

Fig. 5 shows a second circuit arrangement 41 comprising a full-bridge inverter 2 and the lamp switching circuits 6 and 9. Two low-pass filters 42 and 43 filter out the high-frequency components for all the lamp circuits 6 and 9.

Fig. 6 shows a third circuit arrangement 51 comprising the full-bridge inverter 5, 2, the voltage source 3 and two lamp switching circuits 52 and 53. Between the two nodes 25 and 26 in the lamp circuit 52 is connected a capacitor 54, a coil 55 and a capacitor 56 which together work as a low-pass filter, and a low-pressure gas discharge lamp 24 in parallel with capacitor 56. The coil 55 and the capacitor 56 form a resonant circuit 55, 56.

The coil 55 has double the inductance of coil 20, the capacitor 56 half the capacitance of the capacitor 22. There is a voltage drop across the capacitor 56, which drop corresponds to the lamp voltage.

Fig. 7 shows an electrical circuit arrangement 61 with two series-connected capacitors 62, 63 which work for all the lamp circuits 52, 53.

Fig. 8 shows a diagram in which the voltage is plotted against frequency. The AC power gain function of a resonant circuit is shown as a function of the switching frequency. To ignite a low-pressure gas discharge lamp, the full-bridge starts with a starting frequency 71, reduces the switching frequency until the lamp ignites at an ignition frequency 72 and reduces the switching frequency further to an operating frequency 73.

List of reference characters:

1	circuit arrangement
2	full-bridge inverter
3	voltage source
4	low-pass filter
5	low-pass filter
6	lamp switching circuit
7	low-pass filter
8	low-pass filter
9	lamp switching circuit
10	10 electrically conducting line
11	electrically conducting line
12	electronically conducting line
13	control circuit
14	inverter
15	15 inverter
16	power switch
17	power switch
18	power switch
19	power switch
20	20 series coil
21	series coil
22	capacitor
23	capacitor
24	lamp
25	25 node
26	node
31	rectangular signal waveform
32	sinusoidal fundamental wave

33	second sinusoidal fundamental wave
34	voltage amplitude
41	second circuit arrangement
42	low-pass filter
5 43	low-pass filter
51	third circuit arrangement
52	lamp switching circuit
53	lamp switching circuit
54	capacitor
10 55	coil
56	capacitor
61	four circuit arrangement
62	capacitor
63	capacitor
15 71	start frequency
72	ignition frequency
73	operating frequency

CLAIMS:

1. A circuit arrangement (1, 41, 51, 61) for operating one or more low-pressure gas discharge lamps (24), comprising a current converter (14) and a driving device (13) for the current converter (14), characterized in that a second current converter (15) generates a voltage (32, 33) phase-shifted by 180°.

5

2. A circuit arrangement (1) for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps (24) which utilizes a full-bridge switching circuit (2) with power switches (16, 17, 18, 19) as current converters (14, 15) and two resonant circuits (4, 5, 20, 21, 22, 23) per lamp (24), each of the resonant circuits (4, 5, 10 20, 21, 22, 23) having a series-connected coil (20, 21), one series-connected capacitor (4, 5) and one parallel-connected capacitor (22, 23).

3. A circuit arrangement (41) for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps (24), which utilizes a full-bridge switching circuit (2) including power switches (16, 17, 18, 19) as current converters (14, 15), two series-connected capacitors (42, 43) and two resonant circuits (20, 21, 22, 23) per lamp (24), each of the resonant circuits (20, 21, 22 or 23) having one series-connected coil (20, 21) and one parallel-connected capacitor (22, 23).

15 20 4. A circuit arrangement (51) for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps (24) which utilizes a full-bridge switching circuit (2) with power switches (16, 17, 18, 19) as current converters (14, 15) and one resonant circuit (54, 55, 56) per lamp (24), which resonant circuit includes one series-connected coil (55), one series-connected capacitor (54) and one parallel-connected capacitor 25 (56).

5. A circuit arrangement (61) for converting DC current into AC current and for feeding one or more low-pressure gas discharge lamps (24), which utilizes a full-bridge switching circuit (2) with power switches (16, 17, 18, 19) as current converters (14, 15), two

series-connected capacitors (62, 63) and one resonant circuit (55, 56) per lamp (24), which resonant circuit includes a series-connected coil (55) and a parallel-connected capacitor (56).

6. A circuit arrangement as claimed in one of the preceding claims 2-5,
5 characterized in that the parallel-connected capacitor (22, 23, 56) is formed at least partly by a parasitic capacitance between the lamp (24) and a metallic part.

7. A liquid crystal display on which a video signal of a computer or of a television set can be represented, comprising a circuit arrangement (1, 41, 51, 61) as claimed
10 in one of the preceding claims 1-6.

1/6

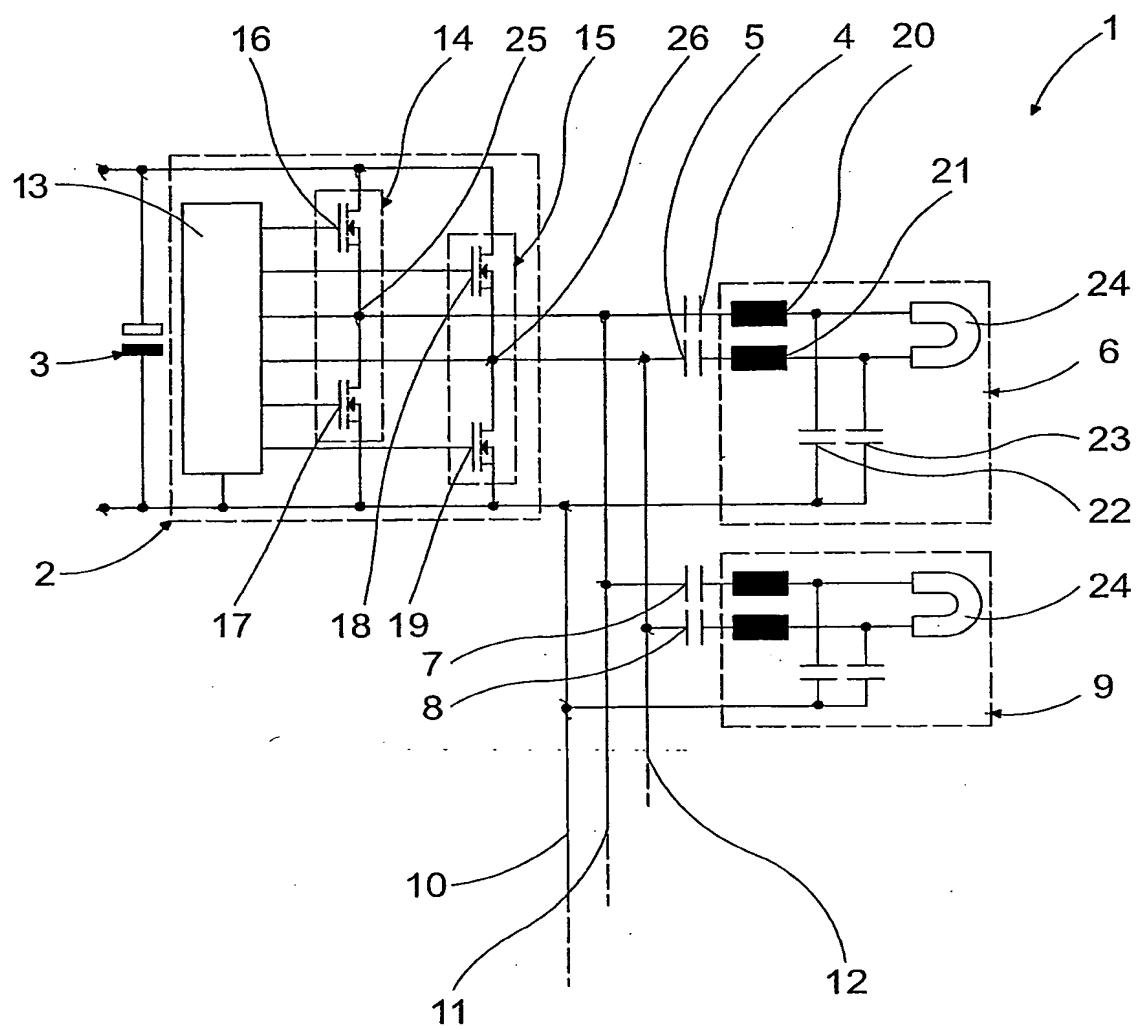


FIG.1

2/6

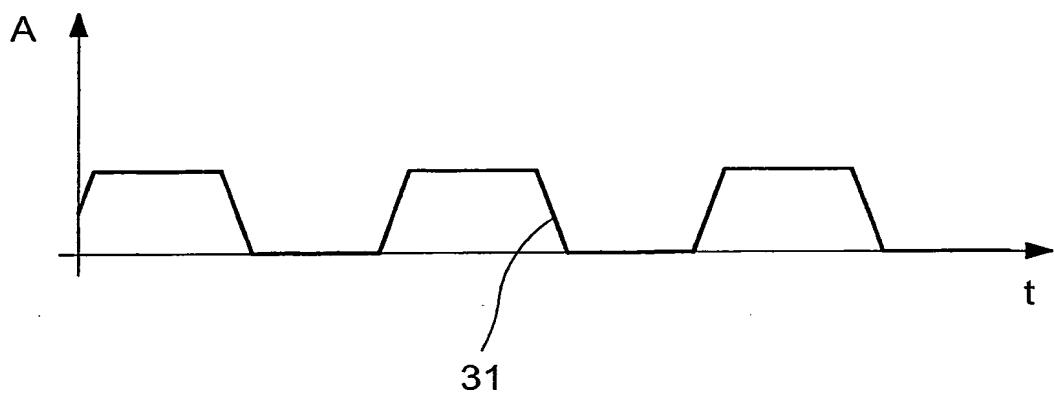


FIG.2

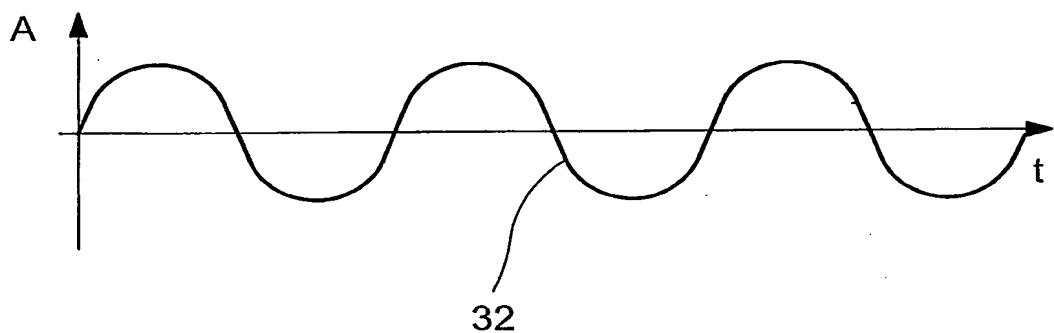


FIG.3

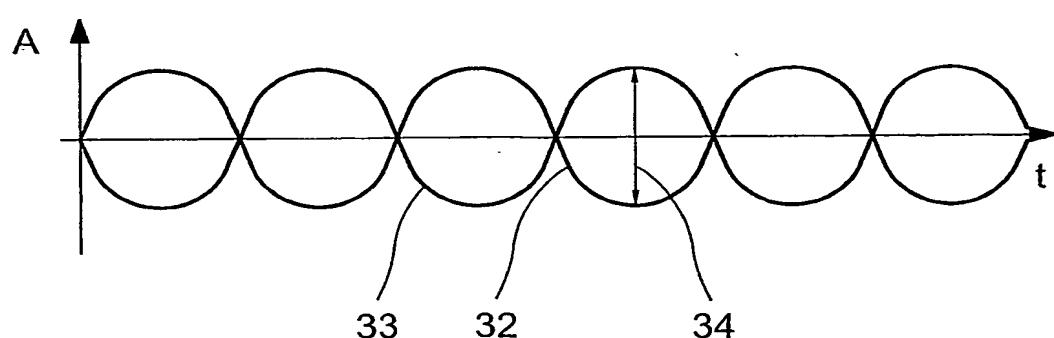


FIG.4

3/6

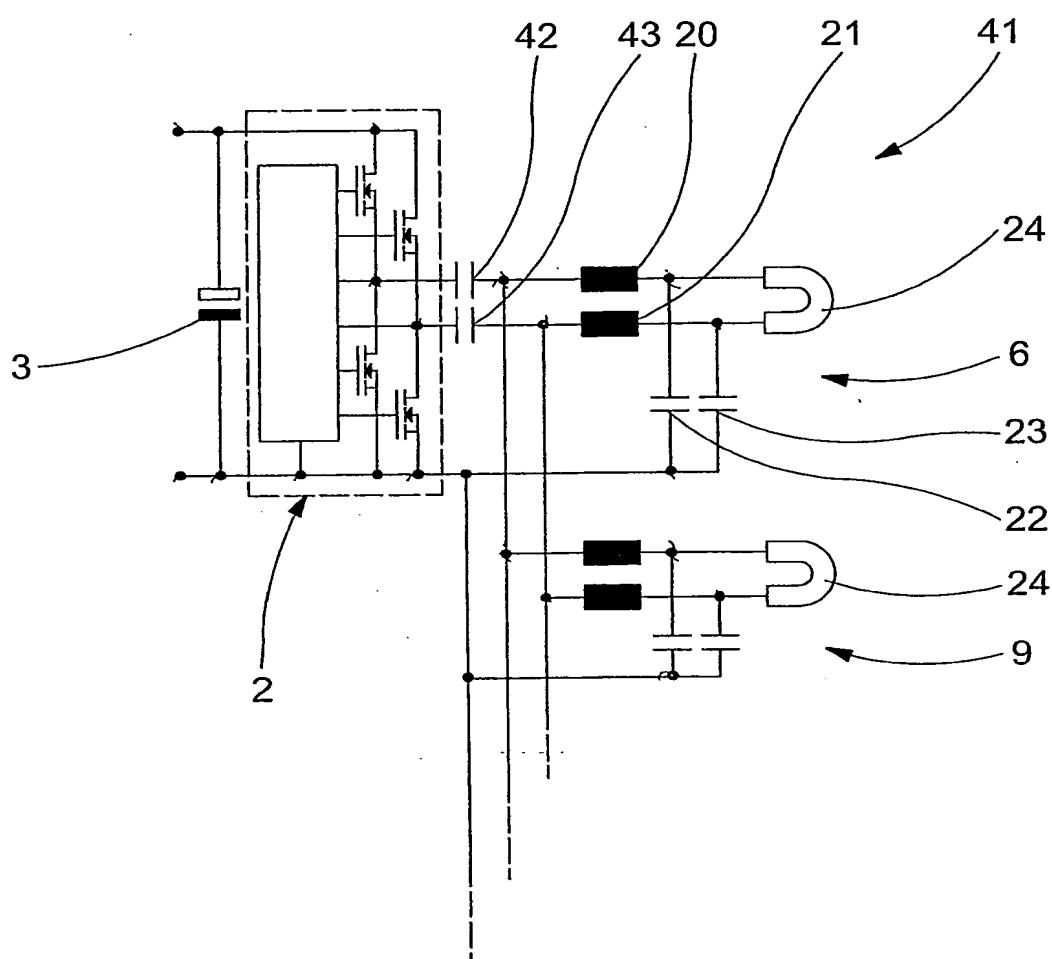


FIG.5

4/6

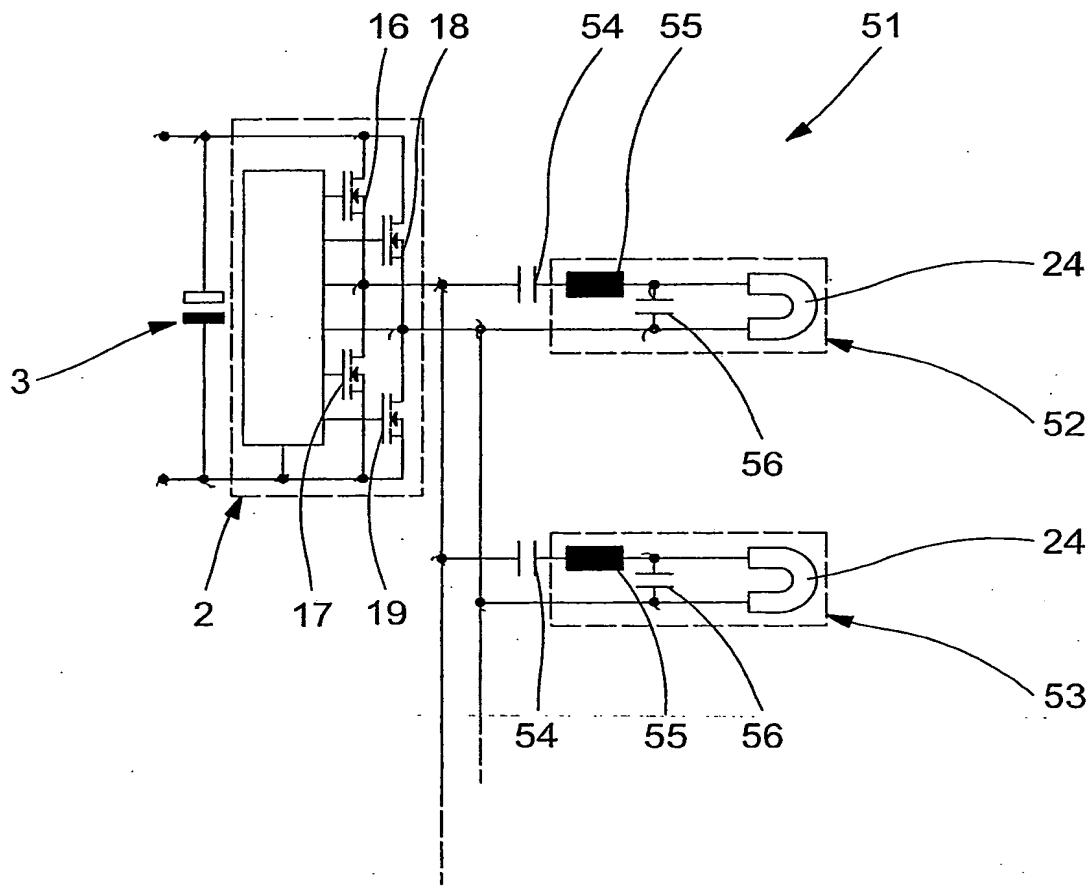


FIG.6

5/6

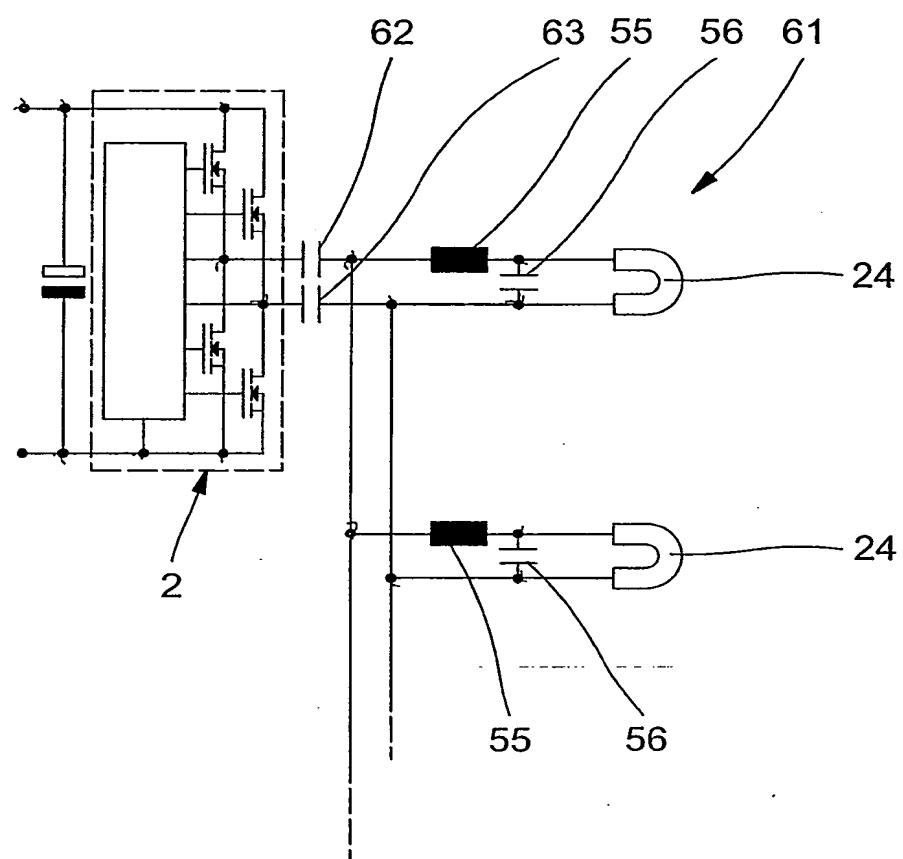


FIG.7

6/6

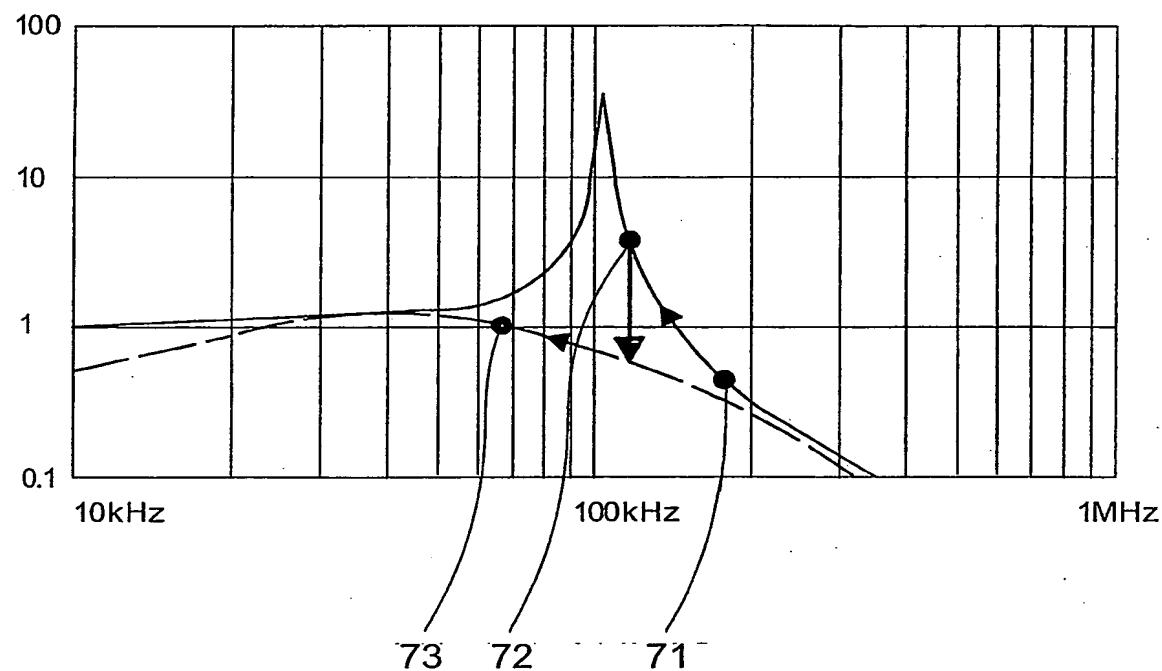


FIG.8

INTERNATIONAL SEARCH REPORT

In - final Application No
PCT/IB 02/05467A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H05B41/282

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H05B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, COMPENDEX, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 615 093 A (NALBANT MEHMET K) 25 March 1997 (1997-03-25) column 1, line 12 -column 11, line 24; figures 1-9	1-5
X	GULES R ET AL: "A 1.2 kW electronic ballast for multiple lamps, with dimming capability and high-power-factor" APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 1999. APEC '99. FOURTEENTH ANNUAL DALLAS, TX, USA 14-18 MARCH 1999, PISCATAWAY, NJ, USA, IEEE, US, 14 March 1999 (1999-03-14), pages 720-726, XP010323608 ISBN: 0-7803-5160-6 page 720 -page 723; figures 1-10	1-5

 Further documents are listed in the continuation of box C. Patent family members are listed in annex.

* Special categories of cited documents :

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

T later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

& document member of the same patent family

Date of the actual completion of the international search

Date of mailing of the international search report

20 March 2003

27/03/2003

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax (+31-70) 340-3016

Authorized officer

Albertsson, E

INTERNATIONAL SEARCH REPORT

I onal Application No
PCT/IB 02/05467

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	TADESSE D ET AL: "A comparison of power circuit topologies and control techniques for a high frequency ballast" INDUSTRY APPLICATIONS SOCIETY ANNUAL MEETING, 1993., CONFERENCE RECORD OF THE 1993 IEEE TORONTO, ONT., CANADA 2-8 OCT. 1993, NEW YORK, NY, USA, IEEE, US, 2 October 1993 (1993-10-02), pages 2341-2347, XP010118644 ISBN: 0-7803-1462-X page 2341 -page 2345; figures 1-6	1-5
X	SPIAZZI G ET AL: "High-quality rectifiers with high-frequency insulation—an overview" INDUSTRIAL ELECTRONICS, 1995. ISIE '95., PROCEEDINGS OF THE IEEE INTERNATIONAL SYMPOSIUM ON ATHENS, GREECE 10-14 JULY 1995, NEW YORK, NY, USA, IEEE, US, 10 July 1995 (1995-07-10), pages 64-71, XP010161393 ISBN: 0-7803-2683-0 page 64 -page 69; figures 1-14	1-5
X	US 5 744 915 A (NILSSON OLE K) 28 April 1998 (1998-04-28) column 2 -column 23; figures 1-14	1
A	DONAHUE J A ET AL: "The LCC inverter as a cold cathode fluorescent lamp driver" APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 1994. APEC '94. CONFERENCE PROCEEDINGS 1994., NINTH ANNUAL ORLANDO, FL, USA 13=17 FEB. 1994, NEW YORK, NY, USA, IEEE, 13 February 1994 (1994-02-13), pages 427-433, XP010118539 ISBN: 0-7803-1456-5 abstract; figures 1-7	1,7
A	LEE S W ET AL: "Simplified control technique for LCD backlight inverter system using the mixed dimming method" APEC 2001. 16TH. ANNUAL IEEE APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION. ANAHEIM, CA, MARCH 4 - 8, 2001, ANNUAL APPLIED POWER ELECTRONICS CONFERENCE, NEW YORK, NY: IEEE, US, vol. 1 OF 2. CONF.16, 4 March 2001 (2001-03-04), pages 447-453, XP010536032 ISBN: 0-7803-6618-2 abstract; figures 1-8	1,7

-/--

INTERNATIONAL SEARCH REPORT

I onal Application No
PCT/IB 02/05467

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	MADER U: "Steady-state analysis of a voltage-fed inverter with second-order network and fluorescent lamp load" APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 1996. APEC '96. CONFERENCE PROCEEDINGS 1996., ELEVENTH ANNUAL SAN JOSE, CA, USA 3-7 MARCH 1996, NEW YORK, NY, USA, IEEE, US, 3 March 1996 (1996-03-03), pages 609-615, XP010159847 ISBN: 0-7803-3044-7 ----	
A	CORREA J ET AL: "A COMPARISON OF LCC AND LC FILTERS FOR ITS APPLICATION IN ELECTRONIC BALLAST FOR METAL-HALIDE LAMPS" 32ND ANNUAL IEEE POWER ELECTRONICS SPECIALISTS CONFERENCE. PESC 2001. CONFERENCE PROCEEDINGS. VANCOUVER, CANADA, JUNE 17 - 21, 2001, ANNUAL POWER ELECTRONICS SPECIALISTS CONFERENCE, NEW YORK, NY: IEEE, US, vol. 1 OF 4. CONF. 32, 17 June 2001 (2001-06-17), pages 114-119, XP001049520 ISBN: 0-7803-7067-8 ----	
A	RIBAS J ET AL: "A NEW DISCHARGE LAMP BALLAST BASED ON A SELF-OSCILLATING FULL-BRIDGE INVERTER INTEGRATED WITH A BUCK-TYPE PFC CIRCUIT" APEC 2001. 16TH. ANNUAL IEEE APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION. ANAHEIM, CA, MARCH 4 - 8, 2001, ANNUAL APPLIED POWER ELECTRONICS CONFERENCE, NEW YORK, NY: IEEE, US, vol. 2 OF 2. CONF. 16, 4 March 2001 (2001-03-04), pages 688-694, XP001049793 ISBN: 0-7803-6618-2 ----	
A	BRANAS C ET AL: "Electronic ballast for 250 W HPS lamps based on the LCC resonant inverter with soft start-up and quasi-optimum control" INDUSTRIAL ELECTRONICS, 1999. ISIE '99. PROCEEDINGS OF THE IEEE INTERNATIONAL SYMPOSIUM ON BLED, SLOVENIA 12-16 JULY 1999, PISCATAWAY, NJ, USA, IEEE, US, 12 July 1999 (1999-07-12), pages 768-773, XP010354016 ISBN: 0-7803-5662-4 ----	

INTERNATIONAL SEARCH REPORT

International Application No
PCT/IB 02/05467

Patent document cited in search report		Publication date		Patent family member(s)		Publication date
US 5615093		A	25-03-1997	TW	400685 B	01-08-2000
US 5744915	A	28-04-1998	US	5428266 A	27-06-1995	
			US	4184128 A	15-01-1980	
			US	5512801 A	30-04-1996	
			US	5471118 A	28-11-1995	
			US	5459375 A	17-10-1995	
			US	5757144 A	26-05-1998	
			US	5469028 A	21-11-1995	
			US	5479074 A	26-12-1995	
			US	6002210 A	14-12-1999	
			US	5446346 A	29-08-1995	
			US	6459213 B1	01-10-2002	
			US	5191262 A	02-03-1993	
			US	5446347 A	29-08-1995	
			US	5757140 A	26-05-1998	
			US	5341067 A	23-08-1994	
			US	5491385 A	13-02-1996	
			US	5343124 A	30-08-1994	
			US	5426347 A	20-06-1995	
			US	5510681 A	23-04-1996	
			US	5371441 A	06-12-1994	
			US	6172464 B1	09-01-2001	
			US	5559393 A	24-09-1996	
			US	6211619 B1	03-04-2001	
			US	5691603 A	25-11-1997	
			US	6198228 B1	06-03-2001	
			US	6211625 B1	03-04-2001	
			US	5640069 A	17-06-1997	
			US	5047690 A	10-09-1991	
			US	5185560 A	09-02-1993	
			US	5166578 A	24-11-1992	
			US	5164637 A	17-11-1992	
			US	6100643 A	08-08-2000	
			US	5214355 A	25-05-1993	
			US	5214356 A	25-05-1993	
			US	5233270 A	03-08-1993	
			US	6144445 A	07-11-2000	
			US	5510680 A	23-04-1996	
			US	5489823 A	06-02-1996	
			US	5416386 A	16-05-1995	
			US	5422546 A	06-06-1995	
			US	5426349 A	20-06-1995	
			US	5440209 A	08-08-1995	
			US	5402043 A	28-03-1995	
			US	5432409 A	11-07-1995	
			US	5481160 A	02-01-1996	
			US	5736819 A	07-04-1998	
			US	4857806 A	15-08-1989	
			US	5438239 A	01-08-1995	
			US	4677345 A	30-06-1987	
			US	4513364 A	23-04-1985	